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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	
				Filing Date	
				First Named Inventor Tadahiko Hisano	
				Group Art Unit	
				Examiner Name	
Sheet 1 of 2				Attorney Docket Number	

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Number	Kind Code ² (if known)		
[Signature]		4377972		O' Neil	03/29/1983
		4817940		Shaw et al.	01/04/1989
		5196994		Tanuma et al.	03/23/1993
		5218686		Thayer	06/08/1993
		5249160		Wu et al.	09/28/1993
		5269015		Shires	12/07/1993
		5361228		Adachi et al.	11/01/1994
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		5418952		Morley et al.	05/23/1995
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		5502733		Kishi et al.	03/26/1996
		5524231		Brown	06/04/1996
		5526490		Nishikawa	06/11/1996
		5548741		Watanabe	08/20/1996
		5625777		Takahashi et al.	04/29/1997
[Signature]		5636348		Buxton et al.	06/03/1997
		5740404		Baji.	04/14/1998
		5381043		Kohiyama et al.	01/10/1995
		5313618		Pawloski.	05/17/1994
		4667088		Krammer et al.	05/19/1987
		5694557		Yang	12/02/1997

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Sheet		2	of	2	Application Number Filing Date First Named Inventor Group Art Unit Examiner Name Attorney Docket Number
				Tadahiko Hisano	

FOREIGN PATENT DOCUMENTS							
Examiner Initials ¹	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	T ²
		Office ³	Number ⁴	Kind Code ⁵ (if known)			
		JP	A-60-215267		Kong K K	10/28/1985	
		JP	A-63-110947		NEC Corp.	07/16/1988	
		JP	A-6-149431		Tokyo Electric Co., Ltd	05/27/1994	
		WO	WO 93/23811		SOUTHWESTER BELL TEC.	11/25/1993	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		80186/80188 Intel User's Manual (Nov., 1994).	
		P. Oguic, "Carte E/S externe pour port parallel", Electronique Radio Plans, No. 567, Feb. 1, 1994, pp. 43-47.	
		B. J. Freeman et al., "Microprocessor-to-microprocessor communications via an 8-bit data bus" IBM Technical Disclosure Bulletin., vol. 25, No. 10, Mar. 1983, pp. 5230-5235.	

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